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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Masashi Kiyose et al. Art Unit : 2816
Patent No. : 6,914,465 Examiner : Minh T. Nguyen
Issue Date : July 5, 2005
Serial No. : 10/634,151
Filed : August 4, 2003
Title : PLL CIRCUIT AND DATA RECORDING CONTROLLER EACH HAVING
DUAL PHASE-LOCKED LOOP

Attn.: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION

Applicants hereby request that a certificate of correction be issued for the above patent in accordance with the attached request.

All errors sought to be corrected were made in printing by the Patent and Trademark Office, and no fee is believed to be due.

Certificate
AUG 05 2005
of Correction

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Page : 2 of 2

Attorney's Docket No.: 10449-
070001 / P1S2003116US

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Respectfully submitted,

Date: July 28, 2005



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,914,465
DATED : JULY 5, 2005
INVENTOR(S) : MASASHI KIOSE AND TAKUYA SHIRAISHI

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please change the title to read: PLL CIRCUIT AND DATA RECORDING
CONTROLLER EACH HAVING DUAL PHASE-LOCKED LOOP

MAILING ADDRESS OF SENDER:

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PATENT No. 6,914,465

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